

This book targets ADC design using TIQ technique. The aim of the book is to provide step by step guidelines to design an ADC using Threshold inverter Quantization Technique. the book comprehensively discusses and analyses Comparator design usin TIQ technique, discusses various encoder topologies to cover various design related issues in Analog VLSI domain. Idea of realization of encoder using psuedo logic CMOS is also presented. in order to present a fair comparison the all the encoders are tested using DC and Transient analysis. Evaluation of the results is presented and finally future evolution is also presented.

Basic methodology to design ADC



Liyaqat Nazir



Liyaqat Nazir, is presently a Ph.D. scholar in National Institute of Technology from India. He has received the B.Tech degree in Electronics and Communications Engineering from the Islamic University of Science and Technology, India, in 2011, He did his M.Tech degree in CIT from NIT srinagar.

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Nazir

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